

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** Takayuki Tomita, et al.      **Examiner:** Unassigned  
**Serial No:** To be assigned      **Art Unit:** Unassigned  
**Filed:** Herewith      **Docket:** 14651  
**For:** SPECTRUM SPREAD RECEIVER      **Dated:** May 25, 2001  
DEVICE

Assistant Commissioner for Patents  
United States Patent and Trademark Office  
Washington, D.C. 20231

**PRELIMINARY AMENDMENT**

Sir:

In connection with the above-identified patent application, kindly enter the following preliminary amendment.

**IN THE SPECIFICATION:**

Please amend the specification as follows:

Please replace the paragraph beginning at page 1, line 18, with the following rewritten paragraph:

--In Fig. 3, a finger processing portion, which is composed of a replica code generator 34, a correlator 35, and a asynchronous detector 36, detects signals at a timing

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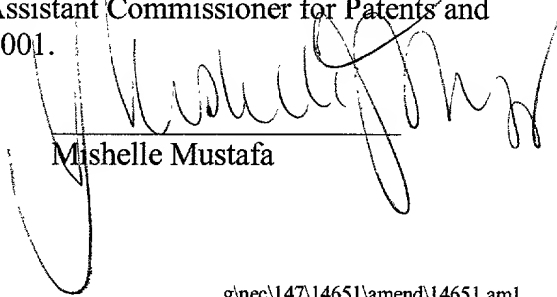
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Dated: May 25, 2001

  
Mishelle Mustafa

allocated by a timing control circuit 33. Demodulated data is output from a RAKE synthesizer 38 by inputting the detected data, whose timings are adjusted by a timing adjustment buffer 37, into the RAKE synthesizer 38. Japanese Unexamined Patent Application, First Publication No. Hei 10-190528 discloses this technique in detail--.

**REMARKS**

Attached hereto is a marked-up version of the changes made to the specification by the current amendment. The attached page is captioned **"Version with markings to show changes made."**

Applicants submit that the foregoing amendment to the specification was made to correct a certain typographical error, and does not introduce new matter into the application. Early and favorable consideration of the present application, as amended herein, is respectfully requested.

Respectfully submitted,



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Encl. (Version with markings to show changes made)

**VERSION WITH MARKINGS TO SHOW CHANGES MADE**

**In the specification:**

Paragraph beginning at page 1, line 18, has been amended as follows:

In Fig. 3, a finger processing portion, which is composed of a replica code generator 34, a correlator 35, and [an] a asynchronous detector 36, detects signals at a timing allocated by a timing control circuit 33. Demodulated data is output from a RAKE synthesizer 38 by inputting the detected data, whose timings are adjusted by a timing adjustment buffer 37, into the RAKE synthesizer 38. Japanese Unexamined Patent Application, First Publication No. Hei 10-190528 discloses this technique in detail.